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BOX PATENT APPLICATION  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Re: Application of Shinji ABE and Shinji UENO  
COMMUNICATION CONTROL SYSTEM AND CONTROL METHOD THEREOF  
Our Ref. Q61931

Dear Sir:

Attached hereto is the application identified above including 95 sheets of the specification and claims, 10 sheets of formal drawings, the executed Assignment and PTO 1595 form, and the executed Declaration and Power of Attorney. Also enclosed is an Information Disclosure Statement with Form PTO-1449 and reference.

The Government filing fee is calculated as follows:

Total claims	47 - 20	=	27	x	\$18.00	=	\$486.00
Independent claims	3 - 3	=		x	\$80.00	=	
Base Fee							\$710.00
<b>TOTAL FILING FEE</b>							<b>\$1196.00</b>
Recordation of Assignment							\$40.00
<b>TOTAL FEE</b>							<b>\$1236.00</b>

Checks for the statutory filing fee of \$1196.00 and Assignment recordation fee of \$40.00 are attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 and any petitions for extension of time under 37 C.F.R. § 1.136 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

Priority is claimed from November 25, 1999 based on Japanese Application No. 333741/99. The priority document is enclosed herewith.

Respectfully submitted,  
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# COMMUNICATION CONTROL SYSTEM AND CONTROL METHOD THEREOF

## BACKGROUND OF THE INVENTION

### FIELD OF THE INVENTION

5           The present invention relates to a communication control system for controlling packet transfer conducted by a plurality of logical channels and, more particularly, to a communication control system for conducting sequential processing and synchronous  
10           processing of transmission data at a high speed.

### DESCRIPTION OF THE RELATED ART

          In a communication control device for providing communication between nodes connected by a virtual channel protocol, at the time of conducting message  
15           transfer and data transfer between nodes by using packets, a logical channel is set up between the nodes by a management layer in charge of transfer control and a descriptor which records information for transmission and reception is prepared for each packet transfer to  
20           activate a transmission and reception control circuit.

          Although the descriptor is analyzed by a data link layer of the communication control device, packet transfer is conducted independently for each logical channel in a conventional device, so that logical  
25           channels are independent of each other.

          Therefore, in a case where sequential or synchronous transfer processing is required between a

plurality of logical channels, a management layer realized by higher-order software etc. monitors the end of packet transfer by such a waiting system as polling or interruption and after receiving a notification of the end of transfer, causes other logical channel to activate transfer.

Here, description will be made of sequential processing and synchronous processing by a conventional communication control system.

Fig. 9 is a flow chart for use in explaining sequential processing by a conventional communication control system, with reference to which description will be made with respect to sequential processing of sequentially transferring two packets, a first packet and a second packet as an example.

With reference to Fig. 9, in the sequential processing of transferring the two packets, first, a higher-order management layer transfers a descriptor of the first packet to the communication control device, a data link layer transfers the first packet by a logical channel of the logical channel number "1" (Step 901) and the management layer waits for transfer confirmation by polling, interruption or the like to confirm the transfer (Step 902). The management layer again transfers a descriptor of the second packet to the communication control device, the data link layer transfers the second packet by a logical channel of the

logical channel number "2" (Step 903) and the management layer again waits for transfer confirmation to confirm transfer (Step 904) to result in normal end. When an error occurs at each of the Steps 902 and 904, the routine proceeds to error processing.

As described in the foregoing, since in conventional sequential processing, a higher-order management layer confirms transfer at every packet transfer, a large overhead is generated such as consumption of CPU resources by polling and a delay caused by notification of an interruption and the subsequent confirmation thereof.

The numbers "1" and "2" of the logical channels are here recited as an example. The reason the different logical channels are used for the first packet and the second packet is that in a case, as the simplest example, where the processing of continuously transferring packets by using the logical channel number "1" is to be executed after the first packet without waiting for a result, if the second packet uses the same logical channel number "1", transfer of the second packet is kept waiting until the result is obtained because of the policy of a scheduler.

Fig. 10 is a flow chart for use in explaining synchronous processing of a plurality of packets by a plurality of logical channels by a conventional communication control system, with reference to which

description is made as an example with respect to synchronous processing of simultaneously transferring two packets, a second packet and a third packet after the transfer of a first packet is completed.

With reference to Fig. 10, in the synchronous processing of the two packets, first, a higher-order management layer transfers a descriptor of the first packet to a communication control device, a data link layer transfers the first packet by a logical channel of the logical channel number "1" (Step 1001) and the management layer waits for transfer confirmation by polling, interruption or the like to confirm the transfer (Step 1001). Subsequently, the management layer transfers descriptors of the second packet and the third packet to the communication control device, the data link layer transfers the second packet by the logical channel number "2" (Step 1003) and then the third packet by the logical channel number "3" (Step 1004) and the management layer waits for confirmation of transfer of the two packets to confirm the transfer (Step 1005) to result in normal end. When an error occurs at each of the Steps 1002 and 1005, the routine proceeds to error processing.

As described in the foregoing, also in conventional synchronous processing, transfer is confirmed by a higher-order management layer at every packet transfer and a time lag is generated in

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Generation of the overhead is caused because as illustrated in the above-described examples of the sequential control and synchronous control in Figs. 9 and 10, even in a case of transmitting each packet according to predetermined rules, a higher-order management layer transfers a descriptor to a communication device every time each packet is to be transferred and after the transmission, confirms the

transfer.

Demanded is a system in which a data link layer takes charge of such processing as sequential transmission of each packet and a higher-order management layer transfers data to be transmitted together with information including the order of the transmission thereof to a communication device in the lump.

One of conventional techniques of making a data link layer execute processing which is ordinarily conducted by a higher-order management layer is the art disclosed in Japanese Patent Laying-Open (Kokai) No. Heisei 9-204376. According to the technique, information about a destination of transfer data which a higher-order management layer transfers to a communication device is described in a form unique to a system and converted into an ordinary address on a network by a data link layer. The conventional technique, however, is intended not to reduction in overhead between a data link layer and a management layer but to distributional processing of communication address management and fails to cope with high-speed sequential processing and synchronous processing of transmission data. Moreover, for making the data link layer to execute complicated processing of address management, complicated device structure is required.

SUMMARY OF THE INVENTION

An object of the present invention is to eliminate the above-described shortcomings of conventional techniques and to provide a communication processing system which enables processing of sequential control, synchronous control, etc. conventionally requiring intervention of processing by a higher-order management layer to be executed at a high speed by a data link layer and also realizes the high-speed processing without requiring the data link layer to have complicated functions and processing.

According to the first aspect of the invention, a communication control system having a data link layer which executes data transfer on a logical channel for controlling packet transfer conducted by a plurality of the logical channels, comprises

means for recording, in a descriptor for recording information regarding transfer by each logical channel, information including information regarding the order of transfer by each the logical channel, wherein

the data link layer includes

means for executing transfer by each the logical channel based on the order of transfer by each the logical channel designated by the descriptor.

In the preferred construction, the descriptor has a stop bit indicating that transfer by the logical channel in question is not to be executed



immediately but to be started on condition that transfer by other logical channel designated is completed, and

the data link layer includes

means for temporarily stopping transfer

5 processing by the logical channel having the stop bit recorded in the descriptor to wait for the transfer starting condition to be fulfilled.

In another preferred construction, the descriptor has

10 an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated, and

the data link layer includes

15 means for monitoring the completion of transfer by the logical channel having the activation bit recorded in the descriptor to determine fulfillment of the transfer starting condition for the other logical channel designated upon the completion of the transfer.

20 In another preferred construction, the descriptor has

a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in

25 question set to be a transfer starting condition, and

the data link layer includes

means for monitoring the completion of transfer

by the logical channel having the number of logical  
channel to be activated recorded in the descriptor to  
determine fulfillment of the transfer starting condition  
for a logical channel indicated by the number of logical  
5 channel to be activated upon the completion of the  
transfer of the logical channel.

In another preferred construction, the descriptor  
has

an activation bit indicating that completion of  
10 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
15 immediately but to be started on condition that transfer  
by other logical channel designated is completed, and

a number of logical channel to be activated for  
specifying other designated logical channel having the  
completion of transfer by the logical channel in  
20 question set to be a transfer starting condition, and

the data link layer includes

means for temporarily stopping transfer  
processing by the logical channel having the stop bit  
recorded in the descriptor to wait for the transfer  
25 starting condition to be fulfilled,

means for monitoring the completion of transfer  
by the logical channel having the activation bit

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5 means for monitoring the completion of transfer  
by the logical channel having the number of logical  
channel to be activated recorded in the descriptor to  
determine fulfillment of the transfer starting condition  
for a logical channel indicated by the number of logical  
10 channel to be activated upon the completion of the  
transfer of the logical channel.

5 a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
starting condition for the logical channel in question,  
specifying the designated other logical channel to be  
monitored, and

means for temporarily stopping transfer by the logical channel having the number of logical channel to be monitored recorded in the descriptor and monitoring the completion of transfer by a logical channel indicated by the number of logical channel to be monitored to determine fulfillment of the transfer starting condition for the logical channel in question

upon the completion of the transfer.

In another preferred construction, the descriptor has

an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated,

a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a transfer starting condition, and

a number of logical channel to be monitored for, in order to monitor the completion of transfer by other designated logical channel which is set to be a transfer starting condition for the logical channel in question, specifying the designated other logical channel to be monitored, and

the data link layer includes

means for monitoring the completion of transfer by the logical channel having the activation bit recorded in the descriptor to determine fulfillment of the transfer starting condition for the other logical channel designated upon the completion of the transfer,

means for monitoring the completion of transfer by the logical channel having the number of logical channel to be activated recorded in the descriptor to determine fulfillment of the transfer starting condition

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for a logical channel indicated by the number of logical channel to be activated upon the completion of the transfer of the logical channel, and

means for temporarily stopping transfer by the logical channel having the number of logical channel to be monitored recorded in the descriptor and monitoring the completion of transfer by a logical channel indicated by the number of logical channel to be monitored to determine fulfillment of the transfer starting condition for the logical channel in question upon the completion of the transfer.

In another preferred construction, the descriptor has

an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated,

a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed,

a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a transfer starting condition, and

a number of logical channel to be monitored for, in order to monitor the completion of transfer by other

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designated logical channel which is set to be a transfer starting condition for the logical channel in question, specifying the designated other logical channel to be monitored, and

5           the data link layer includes

          means for temporarily stopping transfer processing by the logical channel having the stop bit recorded in the descriptor to wait for the transfer starting condition to be fulfilled,

10           means for monitoring the completion of transfer by the logical channel having the activation bit recorded in the descriptor to determine fulfillment of the transfer starting condition for the other logical channel designated upon the completion of the transfer,

15           means for monitoring the completion of transfer by the logical channel having the number of logical channel to be activated recorded in the descriptor to determine fulfillment of the transfer starting condition for a logical channel indicated by the number of logical  
20           channel to be activated upon the completion of the transfer of the logical channel, and

          means for temporarily stopping transfer by the logical channel having the number of logical channel to be monitored recorded in the descriptor and monitoring  
25           the completion of transfer by a logical channel indicated by the number of logical channel to be monitored to determine fulfillment of the transfer

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In another preferred construction, the descriptor has

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means for comparing, at the time of determination

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5           a number of logical channel to be activated for  
specifying other designated logical channel having the  
completion of transfer by the logical channel in  
question set to be a transfer starting condition, and

10        identification value as numerical data, and

means for temporarily stopping transfer

15 starting condition to be fulfilled,

20 channel designated upon the completion of the transfer,

25 for a logical channel indicated by the number of logical  
channel to be activated upon the completion of the  
transfer of the logical channel, and



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the data link layer includes  
means for temporarily stopping transfer by the  
logical channel having the number of logical channel to

be monitored recorded in the descriptor and monitoring  
the completion of transfer by a logical channel  
indicated by the number of logical channel to be  
monitored to determine fulfillment of the transfer  
starting condition for the logical channel in question  
upon the completion of the transfer, and

means for comparing, at the time of determination  
of the transfer starting condition, a value of the  
monitoring identification value of the descriptor of a  
waiting logical channel which is a logical channel on  
the side waiting for the transfer starting condition to  
be fulfilled and a value of the identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to the waiting  
logical channel and whose transfer completion is the  
transfer starting condition for the waiting logical  
channel to determine fulfillment of the transfer  
starting condition only when the value of the monitoring  
identification value and the value of the identification  
value are equal.

According to the second aspect of the invention,  
a method of controlling a communication control system  
having a data link layer which executes data transfer on  
a logical channel for controlling packet transfer  
conducted by a plurality of the logical channels,  
comprising:

the step of recording, in a descriptor for

recording information regarding transfer by each logical channel, information including information regarding the order of transfer by each the logical channel, and

the step of the data link layer of executing transfer by each the logical channel based on the order of transfer by each the logical channel designated by the descriptor.

In the preferred construction, the method of controlling a communication control system further comprising

the step of recording in the descriptor a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed, and

the step of the data link layer of temporarily stopping transfer processing by the logical channel having the stop bit recorded in the descriptor to wait for the transfer starting condition to be fulfilled.

In another preferred construction, the method of controlling a communication control system further comprising

the step of recording in the descriptor an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated, and

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the step of the data link layer of monitoring the completion of transfer by the logical channel having the activation bit recorded in the descriptor to determine fulfillment of the transfer starting condition for the other logical channel designated upon the completion of the transfer.

In another preferred construction, the method of controlling a communication control system further comprising

the step of recording in the descriptor a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a transfer starting condition, and

the step of the data link layer of monitoring the completion of transfer by the logical channel having the number of logical channel to be activated recorded in the descriptor to determine fulfillment of the transfer starting condition for a logical channel indicated by the number of logical channel to be activated upon the completion of the transfer of the logical channel.

In another preferred construction, the method of controlling a communication control system comprising

the step of recording in the descriptor a number of logical channel to be monitored for, in order to monitor the completion of transfer by other designated logical channel which is set to be a transfer starting

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the step of the data link layer of temporarily stopping transfer by the logical channel having the number of logical channel to be monitored recorded in the descriptor and monitoring the completion of transfer by a logical channel indicated by the number of logical channel to be monitored to determine fulfillment of the transfer starting condition for the logical channel in question upon the completion of the transfer.

the step of recording in the descriptor an identification value and a monitoring identification value as numerical data, and

the step of the data link layer of comparing, at the time of determination of the transfer starting condition, a value of the monitoring identification value of the descriptor of a waiting logical channel which is a logical channel on the side waiting for the transfer starting condition to be fulfilled and a value of the identification value of a preceding logical channel which is a logical channel on the side which conducts transfer prior to the waiting logical channel and whose transfer completion is the transfer starting condition for the waiting logical channel to determine

fulfillment of the transfer starting condition only when the value of the monitoring identification value and the value of the identification value are equal.

According to another aspect of the invention, a  
5 computer readable memory storing a control program of a communication control system having a data link layer which executes data transfer on a logical channel for controlling packet transfer conducted by a plurality of the logical channels,

10 the control program comprising  
the step of recording, in a descriptor for recording information regarding transfer by each logical channel, information including information regarding the order of transfer by each the logical channel, and

15 the step of the data link layer of executing transfer by each the logical channel based on the order of transfer by each the logical channel designated by the descriptor.

Other objects, features and advantages of the  
20 present invention will become clear from the detailed description given herebelow.

#### BRIEF DESCRIPTION OF THE DRAWINGS

The present invention will be understood more  
25 fully from the detailed description given herebelow and from the accompanying drawings of the preferred embodiment of the invention, which, however, should not

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Fig. 5 is block diagram showing a structure of a communication control system according to a second embodiment of the present invention;

Fig. 7 is a flow chart for use in explaining processing of communication control according to the second embodiment of the present invention;

Fig. 8 is a flow chart for use in explaining synchronous processing conducted by the communication

control system according to the second embodiment of the present invention;

Fig. 9 is a flow chart for use in explaining sequential processing conducted by a conventional communication control system;

Fig. 10 is a flow chart for use in explaining synchronous processing conducted by a conventional communication control system.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

The preferred embodiment of the present invention will be discussed hereinafter in detail with reference to the accompanying drawings. In the following description, numerous specific details are set forth in order to provide a thorough understanding of the present invention. It will be obvious, however, to those skilled in the art that the present invention may be practiced without these specific details. In other instance, well-known structures are not shown in detail in order to unnecessary obscure the present invention.

A communication control system, at the time of transferring a message and data to a third party by using a plurality of logical channels, sets up a plurality of logical channels with a node of the third party and transmits and receives the message and the data based on information of a descriptor prepared by a management layer.



The descriptor includes a control segment in which control information is described such as the number of a logical channel and a protocol for use in communication and a data segment in which information is described regarding data itself such as an address in a storage unit which stores a message to be transferred.

In the data transfer procedure, first, a management layer designates a logical channel to transfer a descriptor to a data link layer, the data link layer subsequently analyzes the descriptor, sets up an appropriate packet and outputs a message and data to a physical layer to execute data transfer to a node of a third party, and the management layer confirms the transfer to complete the transfer.

In the communication control system of the present invention, with information including that regarding the transfer order of each logical channel recorded in a descriptor for recording information regarding transfer of each logical channel which conducts each packet transfer, a data link layer executes transfer of each logical channel based on the recorded transfer order.

This arrangement enables sequential control and synchronous control between logical channels to be conducted in the data link layer to realize high-speed and precise processing.

A communication control system according to the

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first embodiment of the present invention adopts a system in which recording, in a descriptor of a logical channel of the side which first conducts transfer, the number of a waiting logical channel of the side which starts transfer next after the data transfer is completed enables a data link layer to execute sequential processing of data transfer based on the information. Hereinafter, the method is referred to as a passive system.

Fig. 1 is a block diagram showing a structure of a communication control system according to a first embodiment of the present invention and Fig. 2 is a diagram showing a format of a descriptor of the first embodiment of the present invention.

With reference to Fig. 2, the descriptor of the present embodiment includes a control segment in which control information of data transfer is described and a data segment in which information regarding data to be transferred is described.

The control segment includes a number of logical channel to be used 51 indicative of the number of a logical channel to be used for transfer, a stop bit 52 indicating that transfer is temporarily stopped for the synchronization with other logical channel, an activation bit 53 indicating that the transition of other designated logical channel from a temporarily stopped state to an active state is made after the

completion of the transfer to activate transfer, a  
number of logical channel to be activated 54 indicative  
of the number of a logical channel to be activated, and  
inherent control information 55 indicative of control  
information inherent to a protocol and a device for use  
in communication control between nodes.

The data segment includes a data address 56  
indicative of an address at which data information such  
as a length of data and data are stored.

With reference to Fig. 1, the communication  
control system of the present embodiment includes in a  
communication control device 100, an inter-node  
interface circuit 10, a plurality of logical channel  
first to n-th control devices 20, a scheduler/packet  
generation circuit 30, a packet reception processing  
unit 40, an input data buffer 50, an input data DMA  
(Direct Memory Access) engine 60, an output data buffer  
70 and an output data DMA engine 80.

The inter-node interface 10, which provides the  
interface with a node of a communication partner, is  
equivalent to a physical layer of the communication  
control device.

The logical channel control circuit 20 is  
connected when a logical channel is set up between nodes  
to operate according to a procedure of communication  
between the nodes, that is, a communication protocol for  
use between the nodes such as error control and flow

control. Although the system in the figure includes a number n of the first to the n-th logical channel control circuits 20, the number can be any number as long as the volume of hardware allows and when the circuit has a function of controlling a plurality of logical channels, one circuit is possible.

The input buffer 50 records each of the information 52 to 56 of the descriptor shown in Fig. 2 for each number of logical channel to be used 51. In other words, the descriptor format illustrated in Fig. 2 shows a structure of one entry to the input buffer 50.

The scheduler/packet generation unit 30 has a function of scheduling descriptors basically in the order of their application to the input buffer 50 to activate the input data DMA engine 60 based on control segment information stored in the input buffer 50 and a data address 56 of the data segment and generating a packet from the main body of data transferred from a memory to ultimately allocate the packet to the logical channel circuit 20 indicated by the number of logical channel to be used 51.

In addition, when the stop bit 52 is set at the control segment, the packet transfer is temporarily stopped and the unit 30 refrains from generation of a packet and allocation of the same to the relevant logical channel control circuit 20.

The packet reception processing unit 40 receives

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a data packet, a transmission confirmation notification packet or an error notification packet from a node of a partner and when confirming by the transmission confirmation notification packet that data transfer is normally conducted, notifies a higher-order management layer of the completion of the packet processing, as well as checking a descriptor control segment of a relevant logical channel in the input buffer 50 and when the activation bit 53 is set, resetting the stop bit 52 of a logical channel indicated by the number of logical channel to be activated 54 to release the temporarily stopped state and put the logical channel to the active state. When receiving the error notification packet, the unit 40 gives an error notification to the higher-order management layer through interruption etc.

Data transferred from the partner's node is multiplexed with an output of the logical channel control circuit 20 and stored in the output data buffer 70 and then stored in the memory through the output DMA engine 80. At this time, data packet reception is notified to the higher-order management layer through the packet reception processing unit 40 to notify data arrival.

Fig. 3 is a flow chart for use in explaining processing of (passive system) communication control according to the first embodiment of the present invention.

With reference to Fig. 3, in the processing of the passive system communication control according to the present embodiment, first, a descriptor is stored in the input data buffer 50 (Step 301) and the scheduler/packet generation unit 30 analyzes information of the stored descriptor (Step 302).

Here, in a case where the stop bit 52 is recorded in the descriptor (the stop bit 52 is ON), temporarily stop transfer of a packet indicated by the descriptor (Step 303) and wait for release of the temporary stop (Step 304) to execute transfer after the temporary stop is released (Steps 305 and 306).

On the other hand, in a case where the stop bit 52 is not recorded in the descriptor (the stop bit 52 is OFF), start the transfer immediately (Steps 303 and 306).

In a case where the transfer succeeds, the activation bit 53 recorded in the descriptor (the activation bit 53 is ON) represents that there exists other logical channel being currently at the temporarily stopped state which is to be transferred after the present transfer is completed, and the number of logical channel to be activated 54 indicates such a logical channel. In this case, therefore, release the temporary stop of a logical channel indicated by the number of logical channel to be activated 54 to start the transfer (Steps 308 and 309) and end the transfer processing by the logical channel.

On the other hand, in a case of transfer success, if the activation bit 53 is yet to be recorded in the descriptor (the activation bit 53 is OFF), end the transfer processing by the logical channel.

5           When data transfer at Step 307 fails, execute error processing and in this case, even if the activation bit 53 is recorded, no release of temporary stop of other logical channel is conducted.

10           Next, processing of the present embodiment will be described with respect to a specific case as an example.

15           Fig. 4 is a flow chart for use in explaining sequential processing by the communication control system according the first embodiment of the present invention, which shows an example of the simplest sequential processing of transferring a second packet after a first packet is transferred and normal end thereof is confirmed.

20           With reference to Fig. 4, according to the passive system of the present embodiment, after the first packet is transferred by the logical channel number "1" (Step 401), the second packet can be subsequently transferred by the logical channel number "2" without waiting for the result (Step 402) to lead to  
25           normal end after waiting for only an ultimate result (Step 403).

The foregoing procedure enables more reduction of

overhead for the confirmation of transfer as compared with that by a conventional technique. Although the simplest case is shown as an example here, the more the steps of the sequential processing are, the more effects the processing can achieve.

The passive system sequential processing in the example shown in Fig. 4 will be described in more detail.

Assume in the following description that the first packet is transferred by a logical channel with the logical channel number "1" under the control of the logical channel first control circuit and that the corresponding descriptor is the descriptor A. Similarly, assume that the second packet is transferred by a logical channel with the logical channel number "2" under the control of the logical channel second control circuit and that the corresponding descriptor is the descriptor B.

First, with the activation bit 53 of the descriptor illustrated in Fig. 2 set in order to activate the second packet after normal transfer is completed and with the number of logical channel to be activated 54 set to be "2" for use by the second packet, the descriptor A of the first packet is transferred to the communication control device.

Since the second packet is temporarily stopped until the end of transfer of the first packet, with the stop bit 52 of the descriptor illustrated in Fig. 2 set,



These descriptors A and B are stored in the input buffer 50.

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channel to be activated 54.

Furthermore, the packet reception processing unit 40 resets the stop bit 52 of a control segment of a descriptor having the logical channel number "2". As a result, temporary stop of the descriptor B for transferring the second packet using the logical channel number "2" is released and a packet is generated by the scheduler/packet generation unit 30 and allocated to the logical channel second control circuit, so that the second packet is transferred to the transfer destination node. In the preceding transfer of the first packet, when an error notification packet is transferred from the partner's node, the packet reception processing unit 40 gives an error notification to the higher-order management layer by interruption or the like.

As described in the foregoing, the communication control system of the present embodiment enables management of the order of transfer between logical channels and the like to be conducted by a data link layer without intervention of a higher-order management layer, thereby executing processing such as packet sequential transfer at a high speed without useless processing.

In addition, the communication control system of the present embodiment realizes the above-described effects not by burdening a data link layer with processing of a higher-order management layer but by

such processing suitable for the data link layer as monitoring of transfer end and reference to and updating of data of a descriptor.

Next, second embodiment of the present invention will be described.

A communication control system of the second embodiment of the present invention adopts a system in which in a descriptor of a logical channel of the side conducting transfer after temporary stop, the number of a logical channel of the side starting the preceding transfer whose transfer completion is a condition of the release of the temporary stop is recorded and a data link layer executes sequential processing of data transfer based on thus obtained information. Hereinafter, this system is referred to as an active system.

Fig. 5 is a block diagram showing a structure of the communication control system according to the second embodiment of the present invention, while Fig. 6 is a diagram showing a format of a descriptor according to the second embodiment of the present invention.

With reference to Fig. 6, similarly to the first embodiment, the descriptor of the present embodiment includes a control segment in which control information on data transfer is described and a data segment in which information regarding data to be transferred is described.

The difference from the first embodiment resides

in that the activation bit 53 and the number of logical  
channel to be activated 54 are not required and a number  
of logical channel to be monitored 58 which is the  
number of a logical channel which conducts the preceding  
transfer and whose normal end is to be monitored is  
provided, and the present embodiment includes an  
identification value 57 as a unique value in a logical  
channel for identifying a descriptor and includes a  
monitoring identification value 59 for similarly  
specifying a descriptor of other logical channel.

This is because data indicative of a correlation  
between a side conducting the preceding transfer and a  
waiting side which is held in a descriptor of the  
waiting side is recorded by using the number of logical  
channel to be monitored 58 and the side conducting the  
preceding transfer executes none of the processing of  
the first embodiment for the waiting side, so that there  
is no need of the activation bit 53 and the number of  
logical channel to be activated 54.

With respect to the identification value 57 and  
the monitoring identification value 59, the  
identification value 57 is recorded in a descriptor of  
the side conducting the preceding transfer and the  
monitoring identification value 59 is recorded in a  
descriptor of the waiting side and at the end of  
transfer by the side conducting the preceding transfer,  
data of the identification value 57 and the monitoring

identification value 59 are compared to each other to release temporary stop of the waiting side only when they are equal. This enables correspondence between the side conducting the preceding transfer and the waiting side to be precisely determined.

An example of a structure of an active system communication control device 100a according to the present embodiment shown in Fig. 5 is the same as that of the passive system shown in Fig. 1, with the only difference being that the function of a packet reception processing unit 40a and the contents of a descriptor stored in an input buffer 50a are modified as described above with reference to Fig. 6. In the following, no description will be made of the same functions as those of the passive system according to the first embodiment.

The input buffer 50a records each of the information 52 to 59 of the descriptor shown in Fig. 6 for each number of logical channel to be used 51. In other words, the descriptor format illustrated in Fig. 6 shows a structure of one entry to the input buffer 50a.

The scheduler/packet generation unit 30 has a function of scheduling descriptors basically in the order of their application to the input buffer 50a to activate the input data DMA engine 60 based on control segment information and a data address 56 of the data segment stored in the input buffer 50a and generating a packet from the main body of data transferred from a

memory to ultimately allocate the packet to the logical channel circuit 20 indicated by the number of logical channel to be used 51.

In addition, when the stop bit 52 is set at the control segment, the packet transfer is temporarily stopped and the unit 30 refrains from generation of a packet and allocation of the same to the relevant logical channel control circuit 20.

The packet reception processing unit 40a receives a data packet, a transmission confirmation notification packet or an error notification packet from a node of a partner and when confirming by the transmission confirmation notification packet that data transfer is normally conducted, notifies a higher-order management layer of the completion of the packet processing, as well as searching all the descriptors stored in the input buffer 50a for the number of logical channel to be monitored 58 and the monitoring identification value 59 to find a descriptor whose values coincide with the number of logical channel to be used 51 and the identification value 57 of the descriptor of the packet whose transfer is completed. When there exists a coincident descriptor, reset the stop bit 52 of the control segment of the relevant descriptor to release the temporarily stopped state and enter the active state.

Fig. 7 is a flow chart for use in explaining processing of (active system) communication control



52 is not recorded in the descriptor, start the transfer immediately (Steps 703 and 707).

Then, in a case where data transfer at Step 707 succeeds, if the transfer processing by the logical channel is completed to result in a failure in transfer, execute the error processing.

Next, processing of the present embodiment will be described with respect to a specific case as an example.

Fig. 8 is a flow chart for use in explaining synchronous processing by the communication control system according the second embodiment of the present invention, which shows the simplest example of synchronous processing of simultaneously transferring a second packet and a third packet after a first packet is transferred and normal end thereof is confirmed. Assume that the second packet and the third packet need to be transferred as simultaneously as possible.

With reference to Fig. 8, according to the active system of the present embodiment, after the first packet is transferred with the logical channel number "1" (Step 801), the second packet can be subsequently transferred with the logical channel number "2" (Step 802) and furthermore the third packet can be transferred with the logical channel number "3" (Step 803) without waiting for the result to lead to normal end after waiting for only an ultimate result (Step 804).



The foregoing procedure enables more reduction of overhead for the confirmation of transfer as compared with that by a conventional technique, as well as improving synchronism between the second and the third packets because of high-speed activation in the data link layer.

The active system synchronous processing in the example shown in Fig. 8 will be described in more detail.

Assume in the following description that the first packet is transferred by a logical channel with the logical channel number "1" under the control of the logical channel first control circuit and that the corresponding descriptor is the descriptor A. Similarly, assume that the second packet is transferred by a logical channel with the logical channel number "2" under the control of the logical channel second control circuit and that the corresponding descriptor is the descriptor B, and that the third packet is transferred by a logical channel with the logical channel number "2" under the control of the logical channel third control circuit and that the corresponding descriptor is the descriptor B.

First, in the active system, since the first packet is on the side to be monitored, no special setting of a descriptor is necessary, so that the descriptor is transferred to the communication control device with the number of logical channel to be used "1"

set at the control segment of the descriptor and, for example, "5", set at an arbitrary identification value 57.

Since the second and the third packets are temporarily stopped to have their transfer activated after monitoring the end of normal transfer of the first packet, both of their descriptors are transferred to the communication control device, with the stop bit 52 set at the control segment, the number of logical channel to be monitored 58 set to "1" which is the number of logical channel to be used for the first packet and the monitoring identification value 59 set to "5" which is the descriptor identification value of the first packet.

Since the scheduler/packet generation unit 30 schedules descriptors basically in the order of their storage into the input buffer 50, the unit first generates a packet based on the descriptor A and allocates the packet to the relevant logical channel first control circuit 20.

Since the descriptor has the stop bit 52 set at the control segment, the second packet is temporarily stopped and the unit 30 refrains from generation of a packet and allocation thereof to a logical channel control circuit. The third packet is similarly handled to enter the temporarily stopped state.

Thereafter, upon notification of a transmission confirmation packet for the first packet made by the

partner's node, the packet is notified to the packet reception processing unit 40 through the inter-node interface 10 and the logical channel first control circuit 20.

The packet reception processing unit 40 searches all the descriptors stored in the input buffer 50 for the number of logical channel to be monitored 58 and the monitoring identification value 59 to find a descriptor whose values coincide with the logical channel number "1" and the identification value "5" of the descriptor of the packet whose transfer is completed. In this case, since descriptors of the second packet and the third packet are relevant, reset the stop bit 52 of the control segments of both the descriptors.

As a result, the temporarily stopped state of the descriptors for transferring the second and the third packets using the logical channel numbers "2" and "3" is released, so that packets are generated by the scheduler/packet generation unit 30 and allocated to the logical channel second control circuit and the logical channel third control circuit, respectively, to simultaneously conduct transfer of the second and the third packets to the transfer destination nodes.

When an error notification packet is transferred from the partner's node, the packet reception processing unit 40 gives an error notification to the higher-order management layer by interruption etc.

Thus, in addition to the same effects attained by the first embodiment, the communication control system according to the present embodiment enables such processing as synchronous transfer of packets to be accurately executed at a high speed and with ease.

Moreover, strictly and precisely checking a correspondence of logical channels between the side conducting the preceding transfer and the waiting side is enabled by the method employing collation of the identification value 57.

As other embodiment, the above-described structures of the passive system according to the first embodiment and the active system according to the second embodiment can be freely combined.

More specifically, although the above-described communication control by a data link layer requires a descriptor to have at least one of the number of logical channel to be activated 54 and the number of logical channel to be monitored 58 which are data indicative of a correspondence of logical channels between the side conducting the preceding transfer and the waiting side, each processing using three information, the number of logical channel to be activated 54, the number of logical channel to be monitored 58 and the identification value 57, is independent of each other to allow various combinations.

In addition, in a mode using the number of

logical channel to be activated 54, provision of the activation bit 53 can be omitted. This is because information whether a value is "set/not set" at the number of logical channel to be activated 54 corresponds to information of "ON/OFF" of the activation bit 53.

Similarly, in a mode using the number of logical channel to be monitored 58, provision of the stop bit 52 can be omitted. This is because information whether a value is "set/not set" at the number of logical channel to be monitored 58 corresponds to information of "ON/OFF" of the stop bit 52.

Nevertheless, the stop bit 52 and the activation bit 53 have the advantage in that less resources are required for their provision and they can be processed more easily than a system referring to the above-described number of logical channel to be activated 54 and the number of logical channel to be monitored 58, and they can be used in combination with the system referring to the above-described number of logical channel to be activated 54 and the number of logical channel to be monitored 58 to make deliberate determination.

Furthermore, although in the active system synchronous processing according to the second embodiment, to one logical channel on the side conducting the preceding transfer, the number of logical channel to be monitored 58 of a plurality of logical

channels on the waiting side correspond, such a mode is possible as allows the same logical channel on the waiting side to correspond to the number of logical channel to be activated 54 of a plurality of logical channels on the side conducting the preceding transfer in the passive system of the first embodiment.

In this case, applicable to the transfer of a logical channel on the side conducting the preceding transfer corresponding to the same waiting logical channel are such a method of releasing temporary stop when one transfer of the plurality of logical channels is completed or a method of releasing temporary stop when all the corresponding transfers are completed.

Each function of the communication control system according to the present embodiment can be realized not only by hardware but also by loading a computer program having each function (communication control program) into a memory of a computer processing device. The communication control program is stored in a recording medium (reference numeral) such as a magnetic disc or a semiconductor memory. Then, loading the program from the recording medium into the computer processing device to control operation of the computer processing device realizes the above-described respective functions.

As described in the foregoing, the communication control system of the present invention attains the following effects.

First, overhead caused by intervention of a higher-order management layer at the time of communication processing can be reduced. The reason is that transmission confirmation of transfer and the accompanying activation of subsequent packet transfer which are ordinarily conducted by a higher-order management layer are conducted in a data link layer.

The second effect is that a time lag and overhead caused by transfer activation by a higher-order management layer at the time of synchronous transfer processing between a plurality of logical channels can be reduced. The reason is that synchronous transfer processing of a plurality of packets ordinarily conducted in a higher-order management layer upon normal end of certain packet transfer is realized by holding a plurality of packets in a data link layer in advance to conduct confirmation of normal end and synchronous activation and transfer processing of a plurality of packets in the data link layer.

Although the invention has been illustrated and described with respect to exemplary embodiment thereof, it should be understood by those skilled in the art that the foregoing and various other changes, omissions and additions may be made therein and thereto, without departing from the spirit and scope of the present invention. Therefore, the present invention should not be understood as limited to the specific embodiment set

out above but to include all possible embodiments which can be embodied within a scope encompassed and equivalents thereof with respect to the feature set out in the appended claims.



WHAT IS CLAIMED IS:

1. A communication control system having a data link layer which executes data transfer on a logical channel for controlling packet transfer conducted by a plurality of said logical channels, comprising:

5 means for recording, in a descriptor for recording information regarding transfer by each logical channel, information including information regarding the order of transfer by each said logical channel, wherein

said data link layer includes

10 means for executing transfer by each said logical channel based on the order of transfer by each said logical channel designated by said descriptor.

2. The communication control system as set forth in claim 1, wherein

said descriptor has

5 a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed, and

said data link layer includes

10 means for temporarily stopping transfer processing by said logical channel having said stop bit recorded in said descriptor to wait for said transfer starting condition to be fulfilled.

3. The communication control system as set forth in claim 1, wherein

said descriptor has

an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated, and

said data link layer includes

means for monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer.

4. The communication control system as set forth in claim 1, wherein

said descriptor has

an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated, and

a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed, and

said data link layer includes

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means for temporarily stopping transfer  
processing by said logical channel having said stop bit  
15 recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled, and

means for monitoring the completion of transfer  
by said logical channel having said activation bit  
recorded in said descriptor to determine fulfillment of  
20 said transfer starting condition for said other logical  
channel designated upon the completion of the transfer.

5. The communication control system as set forth in  
claim 1, wherein

said descriptor has

a number of logical channel to be activated for  
5 specifying other designated logical channel having the  
completion of transfer by the logical channel in  
question set to be a transfer starting condition, and

said data link layer includes

means for monitoring the completion of transfer  
10 by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
15 of the transfer of said logical channel.

6. The communication control system as set forth in

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means for monitoring the completion of transfer by said logical channel having said number of logical channel to be activated recorded in said descriptor to determine fulfillment of said transfer starting condition for a logical channel indicated by said number of logical channel to be activated upon the completion of the transfer of said logical channel.

said descriptor has

an activation bit indicating that completion of

5 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated, and

a number of logical channel to be activated for  
specifying other designated logical channel having the  
10 completion of transfer by the logical channel in  
question set to be a transfer starting condition, and

said data link layer includes

means for monitoring the completion of transfer  
by said logical channel having said activation bit  
15 recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,  
and

means for monitoring the completion of transfer  
20 by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
25 of the transfer of said logical channel.

8. The communication control system as set forth in  
claim 1, wherein

said descriptor has

an activation bit indicating that completion of  
5 transfer by the logical channel in question is set to be

a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
10 immediately but to be started on condition that transfer  
by other logical channel designated is completed, and

said data link layer includes  
means for temporarily stopping transfer  
processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

means for monitoring the completion of transfer  
by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
30 determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion

of the transfer of said logical channel.

9. The communication control system as set forth in claim 1, wherein

said descriptor has

a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
starting condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

said data link layer includes

means for temporarily stopping transfer by said  
logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring  
the completion of transfer by a logical channel  
indicated by said number of logical channel to be  
monitored to determine fulfillment of said transfer  
starting condition for the logical channel in question  
upon the completion of the transfer.

10. The communication control system as set forth in claim 1, wherein

said descriptor has

a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
immediately but to be started on condition that transfer

by other logical channel designated is completed, and  
a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
starting condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

said data link layer includes  
means for temporarily stopping transfer  
processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled, and

means for temporarily stopping transfer by said  
logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring  
the completion of transfer by a logical channel  
indicated by said number of logical channel to be  
monitored to determine fulfillment of said transfer  
starting condition for the logical channel in question  
upon the completion of the transfer.

11. The communication control system as set forth in  
claim 1, wherein

said descriptor has  
an activation bit indicating that completion of  
transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel



designated, and

a number of logical channel to be monitored for, in order to monitor the completion of transfer by other designated logical channel which is set to be a transfer starting condition for the logical channel in question, specifying said designated other logical channel to be monitored, and

said data link layer includes

means for monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer, and

means for temporarily stopping transfer by said logical channel having said number of logical channel to be monitored recorded in said descriptor and monitoring the completion of transfer by a logical channel indicated by said number of logical channel to be monitored to determine fulfillment of said transfer starting condition for the logical channel in question upon the completion of the transfer.

12. The communication control system as set forth in claim 1, wherein

said descriptor has

an activation bit indicating that completion of

5 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
10 immediately but to be started on condition that transfer  
by other logical channel designated is completed, and

a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
15 starting condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

said data link layer includes  
means for temporarily stopping transfer  
20 processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

means for monitoring the completion of transfer  
by said logical channel having said activation bit  
25 recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,  
and

means for temporarily stopping transfer by said  
30 logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring

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13.

said descriptor has

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said data link layer includes

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means for temporarily stopping transfer by said logical channel having said number of logical channel to be monitored recorded in said descriptor and monitoring the completion of transfer by a logical channel indicated by said number of logical channel to be monitored to determine fulfillment of said transfer starting condition for the logical channel in question upon the completion of the transfer.

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14. The communication control system as set forth in claim 1, wherein

said descriptor has

a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed,

a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a transfer starting condition, and

a number of logical channel to be monitored for, in order to monitor the completion of transfer by other designated logical channel which is set to be a transfer starting condition for the logical channel in question, specifying said designated other logical channel to be monitored, and

said data link layer includes

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means for temporarily stopping transfer  
20 processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

means for monitoring the completion of transfer  
by said logical channel having said number of logical  
25 channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
of the transfer of said logical channel, and

30 means for temporarily stopping transfer by said  
logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring  
the completion of transfer by a logical channel  
indicated by said number of logical channel to be  
35 monitored to determine fulfillment of said transfer  
starting condition for the logical channel in question  
upon the completion of the transfer.

15. The communication control system as set forth in  
claim 1, wherein

said descriptor has  
an activation bit indicating that completion of  
5 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

a number of logical channel to be activated for  
specifying other designated logical channel having the  
10 completion of transfer by the logical channel in  
question set to be a transfer starting condition, and

a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
15 starting condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

said data link layer includes

means for monitoring the completion of transfer  
20 by said logical channel having said activation bit  
recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,

means for monitoring the completion of transfer  
25 by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
30 of the transfer of said logical channel, and

means for temporarily stopping transfer by said  
logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring  
the completion of transfer by a logical channel

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35 indicated by said number of logical channel to be  
monitored to determine fulfillment of said transfer  
starting condition for the logical channel in question  
upon the completion of the transfer.

16. The communication control system as set forth in  
claim 1, wherein

said descriptor has

5 an activation bit indicating that completion of  
transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

10 a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
immediately but to be started on condition that transfer  
by other logical channel designated is completed,

15 a number of logical channel to be activated for  
specifying other designated logical channel having the  
completion of transfer by the logical channel in  
question set to be a transfer starting condition, and

20 a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
starting condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

said data link layer includes

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means for temporarily stopping transfer  
processing by said logical channel having said stop bit  
25 recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

means for monitoring the completion of transfer  
by said logical channel having said activation bit  
recorded in said descriptor to determine fulfillment of  
30 said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,

means for monitoring the completion of transfer  
by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
35 determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
of the transfer of said logical channel, and

means for temporarily stopping transfer by said  
40 logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring  
the completion of transfer by a logical channel  
indicated by said number of logical channel to be  
monitored to determine fulfillment of said transfer  
45 starting condition for the logical channel in question  
upon the completion of the transfer.

17. The communication control system as set forth in  
claim 1, wherein

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identification value as numerical data, and

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said data link layer includes

means for temporarily stopping transfer

processing by said logical channel having said stop bit

recorded in said descriptor to wait for said transfer

starting condition to be fulfilled, and

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means for comparing, at the time of determination

of said transfer starting condition, a value of said

monitoring identification value of said descriptor of a

waiting logical channel which is a logical channel on

the side waiting for said transfer starting condition to

20

be fulfilled and a value of said identification value of

a preceding logical channel which is a logical channel

on the side which conducts transfer prior to said

waiting logical channel and whose transfer completion is

said transfer starting condition for said waiting

25

logical channel to determine fulfillment of said

transfer starting condition only when the value of said

monitoring identification value and the value of said

identification value are equal.

19. The communication control system as set forth in  
claim 1, wherein

said descriptor has

an activation bit indicating that completion of

5

transfer by the logical channel in question is set to be

a transfer starting condition for other logical channel

designated, and

an identification value and a monitoring  
identification value as numerical data, and

10           said data link layer includes

means for monitoring the completion of transfer  
by said logical channel having said activation bit  
recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
15       channel designated upon the completion of the transfer,  
and

means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
monitoring identification value of said descriptor of a  
20       waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to  
be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to said  
25       waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
monitoring identification value and the value of said  
30       identification value are equal.

20.       The communication control system as set forth in  
claim 1, wherein

said descriptor has

an activation bit indicating that completion of  
5 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
10 immediately but to be started on condition that transfer  
by other logical channel designated is completed, and

an identification value and a monitoring  
identification value as numerical data, and

said data link layer includes  
15 means for temporarily stopping transfer  
processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

means for monitoring the completion of transfer  
20 by said logical channel having said activation bit  
recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,  
and

25 means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
monitoring identification value of said descriptor of a  
waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to

30 be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to said  
waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
35 logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
monitoring identification value and the value of said  
identification value are equal.

21. The communication control system as set forth in  
claim 1, wherein

said descriptor has

5 a number of logical channel to be activated for  
specifying other designated logical channel having the  
completion of transfer by the logical channel in  
question set to be a transfer starting condition, and

an identification value and a monitoring  
identification value as numerical data, and

10 said data link layer includes

means for monitoring the completion of transfer  
by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
15 condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
of the transfer of said logical channel, and

means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
20 monitoring identification value of said descriptor of a  
waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to  
be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
25 on the side which conducts transfer prior to said  
waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
30 monitoring identification value and the value of said  
identification value are equal.

22. The communication control system as set forth in  
claim 1, wherein

said descriptor has  
a stop bit indicating that transfer by the  
5 logical channel in question is not to be executed  
immediately but to be started on condition that transfer  
by other logical channel designated is completed,

a number of logical channel to be activated for  
specifying other designated logical channel having the  
10 completion of transfer by the logical channel in  
question set to be a transfer starting condition, and  
an identification value and a monitoring

identification value as numerical data, and

said data link layer includes

15 means for temporarily stopping transfer  
processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

20 means for monitoring the completion of transfer  
by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
25 of the transfer of said logical channel, and

means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
monitoring identification value of said descriptor of a  
waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to  
30 be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to said  
waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
35 logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
monitoring identification value and the value of said  
identification value are equal.

23. The communication control system as set forth in claim 1, wherein

an activation bit indicating that completion of  
5 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a transfer starting condition, and

an identification value and a monitoring  
identification value as numerical data, and

15 means for monitoring the completion of transfer  
by said logical channel having said activation bit  
recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,

20 means for monitoring the completion of transfer  
by said logical channel having said number of logical  
channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
25 of logical channel to be activated upon the completion  
of the transfer of said logical channel, and



means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
monitoring identification value of said descriptor of a  
30 waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to  
be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to said  
35 waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
monitoring identification value and the value of said  
40 identification value are equal.

24. The communication control system as set forth in  
claim 1, wherein

said descriptor has  
an activation bit indicating that completion of  
5 transfer by the logical channel in question is set to be  
a transfer starting condition for other logical channel  
designated,

a stop bit indicating that transfer by the  
logical channel in question is not to be executed  
10 immediately but to be started on condition that transfer  
by other logical channel designated is completed,

a number of logical channel to be activated for

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specifying other designated logical channel having the  
completion of transfer by the logical channel in

15 question set to be a transfer starting condition, and  
an identification value and a monitoring  
identification value as numerical data, and

said data link layer includes

means for temporarily stopping transfer

20 processing by said logical channel having said stop bit  
recorded in said descriptor to wait for said transfer  
starting condition to be fulfilled,

means for monitoring the completion of transfer  
by said logical channel having said activation bit

25 recorded in said descriptor to determine fulfillment of  
said transfer starting condition for said other logical  
channel designated upon the completion of the transfer,

means for monitoring the completion of transfer  
by said logical channel having said number of logical

30 channel to be activated recorded in said descriptor to  
determine fulfillment of said transfer starting  
condition for a logical channel indicated by said number  
of logical channel to be activated upon the completion  
of the transfer of said logical channel, and

35 means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
monitoring identification value of said descriptor of a  
waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to

40 be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to said  
waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
45 logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
monitoring identification value and the value of said  
identification value are equal.

25. The communication control system as set forth in  
claim 1, wherein

said descriptor has

5 a number of logical channel to be monitored for,  
in order to monitor the completion of transfer by other  
designated logical channel which is set to be a transfer  
starting condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

10 an identification value and a monitoring  
identification value as numerical data, and

said data link layer includes

15 means for temporarily stopping transfer by said  
logical channel having said number of logical channel to  
be monitored recorded in said descriptor and monitoring  
the completion of transfer by a logical channel  
indicated by said number of logical channel to be

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monitored to determine fulfillment of said transfer  
starting condition for the logical channel in question  
upon the completion of the transfer, and

means for comparing, at the time of determination  
of said transfer starting condition, a value of said  
monitoring identification value of said descriptor of a  
waiting logical channel which is a logical channel on  
the side waiting for said transfer starting condition to  
be fulfilled and a value of said identification value of  
a preceding logical channel which is a logical channel  
on the side which conducts transfer prior to said  
waiting logical channel and whose transfer completion is  
said transfer starting condition for said waiting  
logical channel to determine fulfillment of said  
transfer starting condition only when the value of said  
monitoring identification value and the value of said  
identification value are equal.

26. A method of controlling a communication control  
system having a data link layer which executes data  
transfer on a logical channel for controlling packet  
transfer conducted by a plurality of said logical  
channels, comprising the steps of:

the step of recording, in a descriptor for  
recording information regarding transfer by each logical  
channel, information including information regarding the  
order of transfer by each said logical channel, and

10

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to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer.

29. The method of controlling a communication control system as set forth in claim 26, further comprising:

the step of recording in said descriptor a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed,

the step of recording in said descriptor an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated, and

the steps of said data link layer of:

temporarily stopping transfer processing by said logical channel having said stop bit recorded in said descriptor to wait for said transfer starting condition to be fulfilled, and

monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer.

30. The method of controlling a communication control system as set forth in claim 26, further comprising:

the step of recording in said descriptor a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a transfer starting condition, and

the step of said data link layer of monitoring the completion of transfer by said logical channel having said number of logical channel to be activated recorded in said descriptor to determine fulfillment of said transfer starting condition for a logical channel indicated by said number of logical channel to be activated upon the completion of the transfer of said logical channel.

31. The method of controlling a communication control system as set forth in claim 26, comprising:

the step of recording in said descriptor a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed,

the step of recording in said descriptor a number of logical channel to be activated for specifying other designated logical channel having the completion of transfer by the logical channel in question set to be a





logical channel having said activation bit recorded in  
15 said descriptor to determine fulfillment of said  
transfer starting condition for said other logical  
channel designated upon the completion of the transfer,  
and

monitoring the completion of transfer by said  
20 logical channel having said number of logical channel to  
be activated recorded in said descriptor to determine  
fulfillment of said transfer starting condition for a  
logical channel indicated by said number of logical  
channel to be activated upon the completion of the  
25 transfer of said logical channel.

33. The method of controlling a communication control  
system as set forth in claim 26, comprising:

the step of recording in said descriptor a stop  
bit indicating that transfer by the logical channel in  
5 question is not to be executed immediately but to be  
started on condition that transfer by other logical  
channel designated is completed,

the step of recording in said descriptor an  
activation bit indicating that completion of transfer by  
10 the logical channel in question is set to be a transfer  
starting condition for other logical channel designated,

the step of recording in said descriptor a number  
of logical channel to be activated for specifying other  
designated logical channel having the completion of

15 transfer by the logical channel in question set to be a  
transfer starting condition, and

the steps of said data link layer of:

temporarily stopping transfer processing by said  
logical channel having said stop bit recorded in said  
20 descriptor to wait for said transfer starting condition  
to be fulfilled,

monitoring the completion of transfer by said  
logical channel having said activation bit recorded in  
said descriptor to determine fulfillment of said  
25 transfer starting condition for said other logical  
channel designated upon the completion of the transfer,  
and

monitoring the completion of transfer by said  
logical channel having said number of logical channel to  
30 be activated recorded in said descriptor to determine  
fulfillment of said transfer starting condition for a  
logical channel indicated by said number of logical  
channel to be activated upon the completion of the  
transfer of said logical channel.

35

34. The method of controlling a communication control  
system as set forth in claim 26, comprising:

the step of recording in said descriptor a number  
of logical channel to be monitored for, in order to  
5 monitor the completion of transfer by other designated  
logical channel which is set to be a transfer starting

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condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

10           the step of said data link layer of temporarily  
stopping transfer by said logical channel having said  
number of logical channel to be monitored recorded in  
said descriptor and monitoring the completion of  
transfer by a logical channel indicated by said number  
15           of logical channel to be monitored to determine  
fulfillment of said transfer starting condition for the  
logical channel in question upon the completion of the  
transfer.

35.       The method of controlling a communication control  
system as set forth in claim 26, comprising:

          the step of recording in said descriptor a stop  
bit indicating that transfer by the logical channel in  
5           question is not to be executed immediately but to be  
started on condition that transfer by other logical  
channel designated is completed,

          the step of recording in said descriptor a number  
of logical channel to be monitored for, in order to  
10           monitor the completion of transfer by other designated  
logical channel which is set to be a transfer starting  
condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

15           the steps of said data link layer of:  
            temporarily stopping transfer processing by said  
logical channel having said stop bit recorded in said  
descriptor to wait for said transfer starting condition  
to be fulfilled, and

20           temporarily stopping transfer by said logical  
channel having said number of logical channel to be  
monitored recorded in said descriptor and monitoring the  
completion of transfer by a logical channel indicated by  
said number of logical channel to be monitored to  
25           determine fulfillment of said transfer starting  
condition for the logical channel in question upon the  
completion of the transfer.

36.       The method of controlling a communication control  
system as set forth in claim 26, comprising:

            the step of recording in said descriptor an  
activation bit indicating that completion of transfer by  
5           the logical channel in question is set to be a transfer  
starting condition for other logical channel designated,

            the step of recording in said descriptor a number  
of logical channel to be monitored for, in order to  
monitor the completion of transfer by other designated  
10           logical channel which is set to be a transfer starting  
condition for the logical channel in question,  
specifying said designated other logical channel to be  
monitored, and

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the steps of said data link layer of:

15            monitoring the completion of transfer by said  
logical channel having said activation bit recorded in  
said descriptor to determine fulfillment of said  
transfer starting condition for said other logical  
channel designated upon the completion of the transfer,  
20            and

             temporarily stopping transfer by said logical  
channel having said number of logical channel to be  
monitored recorded in said descriptor and monitoring the  
completion of transfer by a logical channel indicated by  
25            said number of logical channel to be monitored to  
determine fulfillment of said transfer starting  
condition for the logical channel in question upon the  
completion of the transfer.

37.        The method of controlling a communication control  
system as set forth in claim 26, comprising:

             the step of recording in said descriptor a stop  
bit indicating that transfer by the logical channel in  
5            question is not to be executed immediately but to be  
started on condition that transfer by other logical  
channel designated is completed,

             the step of recording in said descriptor an  
activation bit indicating that completion of transfer by  
10            the logical channel in question is set to be a transfer  
starting condition for other logical channel designated,

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the step of recording in said descriptor a number of logical channel to be monitored for, in order to monitor the completion of transfer by other designated logical channel which is set to be a transfer starting condition for the logical channel in question, specifying said designated other logical channel to be monitored, and

the steps of said data link layer of:

temporarily stopping transfer processing by said logical channel having said stop bit recorded in said descriptor to wait for said transfer starting condition to be fulfilled,

monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer, and

temporarily stopping transfer by said logical channel having said number of logical channel to be monitored recorded in said descriptor and monitoring the completion of transfer by a logical channel indicated by said number of logical channel to be monitored to determine fulfillment of said transfer starting condition for the logical channel in question upon the completion of the transfer.

38. The method of controlling a communication control system as set forth in claim 26, comprising:

the step of recording in said descriptor an identification value and a monitoring identification value as numerical data, and

the step of said data link layer of comparing, at the time of determination of said transfer starting condition, a value of said monitoring identification value of said descriptor of a waiting logical channel which is a logical channel on the side waiting for said transfer starting condition to be fulfilled and a value of said identification value of a preceding logical channel which is a logical channel on the side which conducts transfer prior to said waiting logical channel and whose transfer completion is said transfer starting condition for said waiting logical channel to determine fulfillment of said transfer starting condition only when the value of said monitoring identification value and the value of said identification value are equal.

39. The method of controlling a communication control system as set forth in claim 26, comprising:

the step of recording in said descriptor a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed,

the step of recording in said descriptor an  
identification value and a monitoring identification  
value as numerical data, and

the steps of said data link layer of:

temporarily stopping transfer processing by said  
logical channel having said stop bit recorded in said  
descriptor to wait for said transfer starting condition  
to be fulfilled, and

comparing, at the time of determination of said  
transfer starting condition, a value of said monitoring  
identification value of said descriptor of a waiting  
logical channel which is a logical channel on the side  
waiting for said transfer starting condition to be  
fulfilled and a value of said identification value of a  
preceding logical channel which is a logical channel on  
the side which conducts transfer prior to said waiting  
logical channel and whose transfer completion is said  
transfer starting condition for said waiting logical  
channel to determine fulfillment of said transfer  
starting condition only when the value of said  
monitoring identification value and the value of said  
identification value are equal.

40. The method of controlling a communication control  
system as set forth in claim 26, comprising:

the step of recording in said descriptor an  
activation bit indicating that completion of transfer by



5 the logical channel in question is set to be a transfer starting condition for other logical channel designated,

the step of recording in said descriptor an identification value and a monitoring identification value as numerical data, and

10 the steps of said data link layer of:

monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer, and

15 comparing, at the time of determination of said transfer starting condition, a value of said monitoring identification value of said descriptor of a waiting logical channel which is a logical channel on the side waiting for said transfer starting condition to be fulfilled and a value of said identification value of a preceding logical channel which is a logical channel on the side which conducts transfer prior to said waiting

20 logical channel and whose transfer completion is said transfer starting condition for said waiting logical channel to determine fulfillment of said transfer starting condition only when the value of said monitoring identification value and the value of said

25 identification value are equal.

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41. The method of controlling a communication control system as set forth in claim 26, comprising:

the step of recording in said descriptor a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed,

the step of recording in said descriptor an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated,

the step of recording in said descriptor an identification value and a monitoring identification value as numerical data, and

the steps of said data link layer of:

temporarily stopping transfer processing by said logical channel having said stop bit recorded in said descriptor to wait for said transfer starting condition to be fulfilled,

monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor to determine fulfillment of said transfer starting condition for said other logical channel designated upon the completion of the transfer, and

comparing, at the time of determination of said transfer starting condition, a value of said monitoring

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30 identification value of said descriptor of a waiting  
logical channel which is a logical channel on the side  
waiting for said transfer starting condition to be  
fulfilled and a value of said identification value of a  
preceding logical channel which is a logical channel on  
the side which conducts transfer prior to said waiting  
logical channel and whose transfer completion is said  
35 transfer starting condition for said waiting logical  
channel to determine fulfillment of said transfer  
starting condition only when the value of said  
monitoring identification value and the value of said  
identification value are equal.

40  
42. A computer readable memory storing a control  
program of a communication control system having a data  
link layer which executes data transfer on a logical  
channel for controlling packet transfer conducted by a  
5 plurality of said logical channels,

said control program comprising:  
the step of recording, in a descriptor for  
recording information regarding transfer by each logical  
channel, information including information regarding the  
10 order of transfer by each said logical channel, and

the step of said data link layer of executing  
transfer by each said logical channel based on the order  
of transfer by each said logical channel designated by  
said descriptor.

15

43. The computer readable memory storing a control program of a communication control system as set forth in claim 42,

said control program further comprising:

5

the step of recording in said descriptor a stop bit indicating that transfer by the logical channel in question is not to be executed immediately but to be started on condition that transfer by other logical channel designated is completed, and

10

the step of said data link layer of temporarily stopping transfer processing by said logical channel having said stop bit recorded in said descriptor to wait for said transfer starting condition to be fulfilled.

44. The computer readable memory storing a control program of a communication control system as set forth in claim 42,

said control program further comprising:

5

the step of recording in said descriptor an activation bit indicating that completion of transfer by the logical channel in question is set to be a transfer starting condition for other logical channel designated, and

10

the step of said data link layer of monitoring the completion of transfer by said logical channel having said activation bit recorded in said descriptor

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to determine fulfillment of said transfer starting  
condition for said other logical channel designated upon  
15 the completion of the transfer.

45. The computer readable memory storing a control  
program of a communication control system as set forth  
in claim 42,

said control program further comprising:

5 the step of recording in said descriptor a number  
of logical channel to be activated for specifying other  
designated logical channel having the completion of  
transfer by the logical channel in question set to be a  
transfer starting condition, and

10 the step of said data link layer of monitoring  
the completion of transfer by said logical channel  
having said number of logical channel to be activated  
recorded in said descriptor to determine fulfillment of  
said transfer starting condition for a logical channel  
15 indicated by said number of logical channel to be  
activated upon the completion of the transfer of said  
logical channel.

46. The computer readable memory storing a control  
program of a communication control system as set forth  
in claim 42,

said control program comprising:

5 the step of recording in said descriptor a number

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of logical channel to be monitored for, in order to  
monitor the completion of transfer by other designated  
logical channel which is set to be a transfer starting  
condition for the logical channel in question,  
10 specifying said designated other logical channel to be  
monitored, and

the step of said data link layer of temporarily  
stopping transfer by said logical channel having said  
number of logical channel to be monitored recorded in  
15 said descriptor and monitoring the completion of  
transfer by a logical channel indicated by said number  
of logical channel to be monitored to determine  
fulfillment of said transfer starting condition for the  
logical channel in question upon the completion of the  
20 transfer.

47. The computer readable memory storing a control  
program of a communication control system as set forth  
in claim 42,

said control program comprising:

5 the step of recording in said descriptor an  
identification value and a monitoring identification  
value as numerical data, and

the step of said data link layer of comparing, at  
the time of determination of said transfer starting  
10 condition, a value of said monitoring identification  
value of said descriptor of a waiting logical channel

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which is a logical channel on the side waiting for said  
transfer starting condition to be fulfilled and a value  
of said identification value of a preceding logical  
15 channel which is a logical channel on the side which  
conducts transfer prior to said waiting logical channel  
and whose transfer completion is said transfer starting  
condition for said waiting logical channel to determine  
fulfillment of said transfer starting condition only  
20 when the value of said monitoring identification value  
and the value of said identification value are equal.

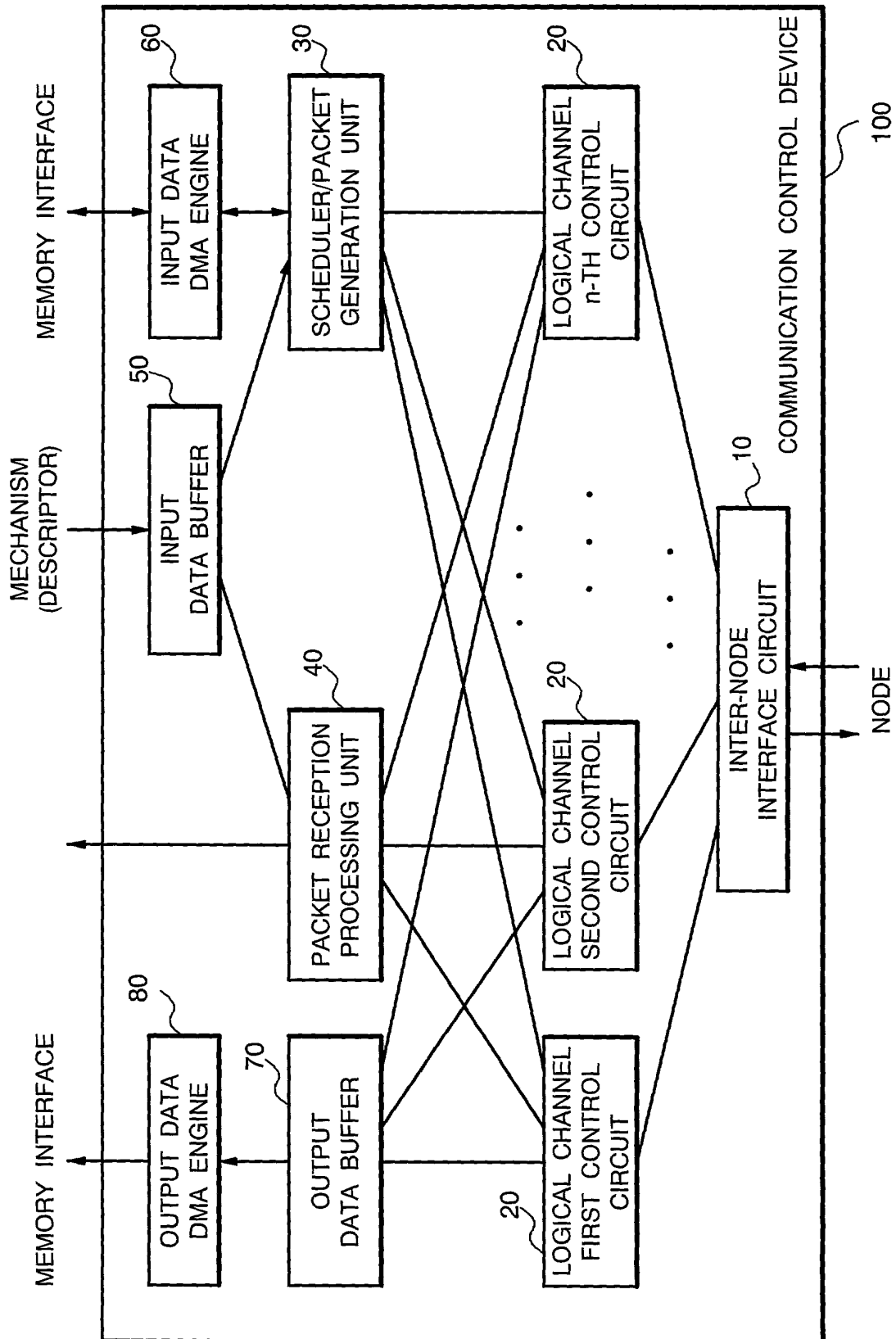
00227T" 0622760

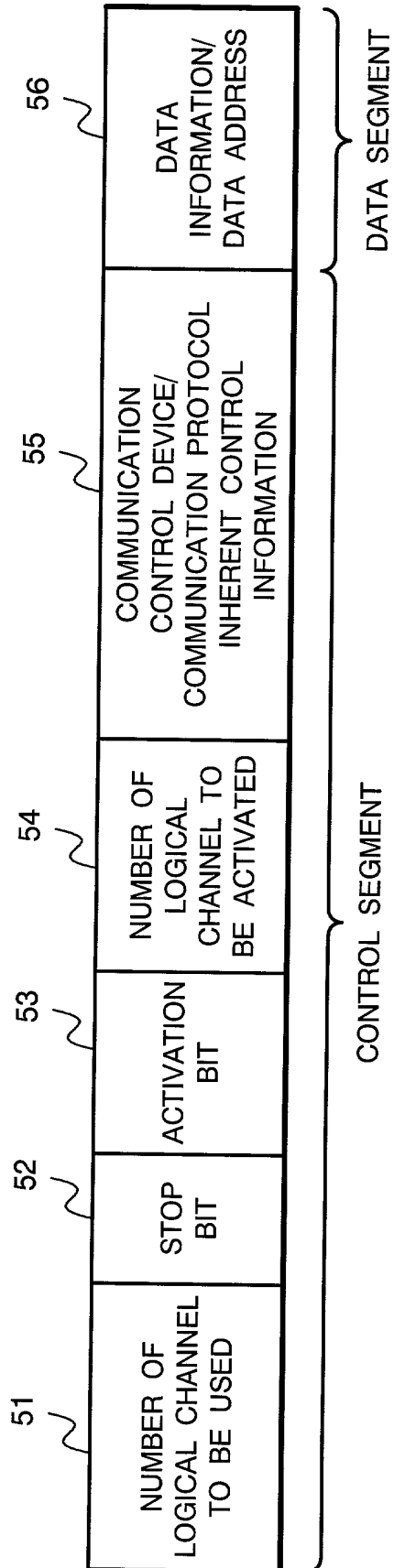
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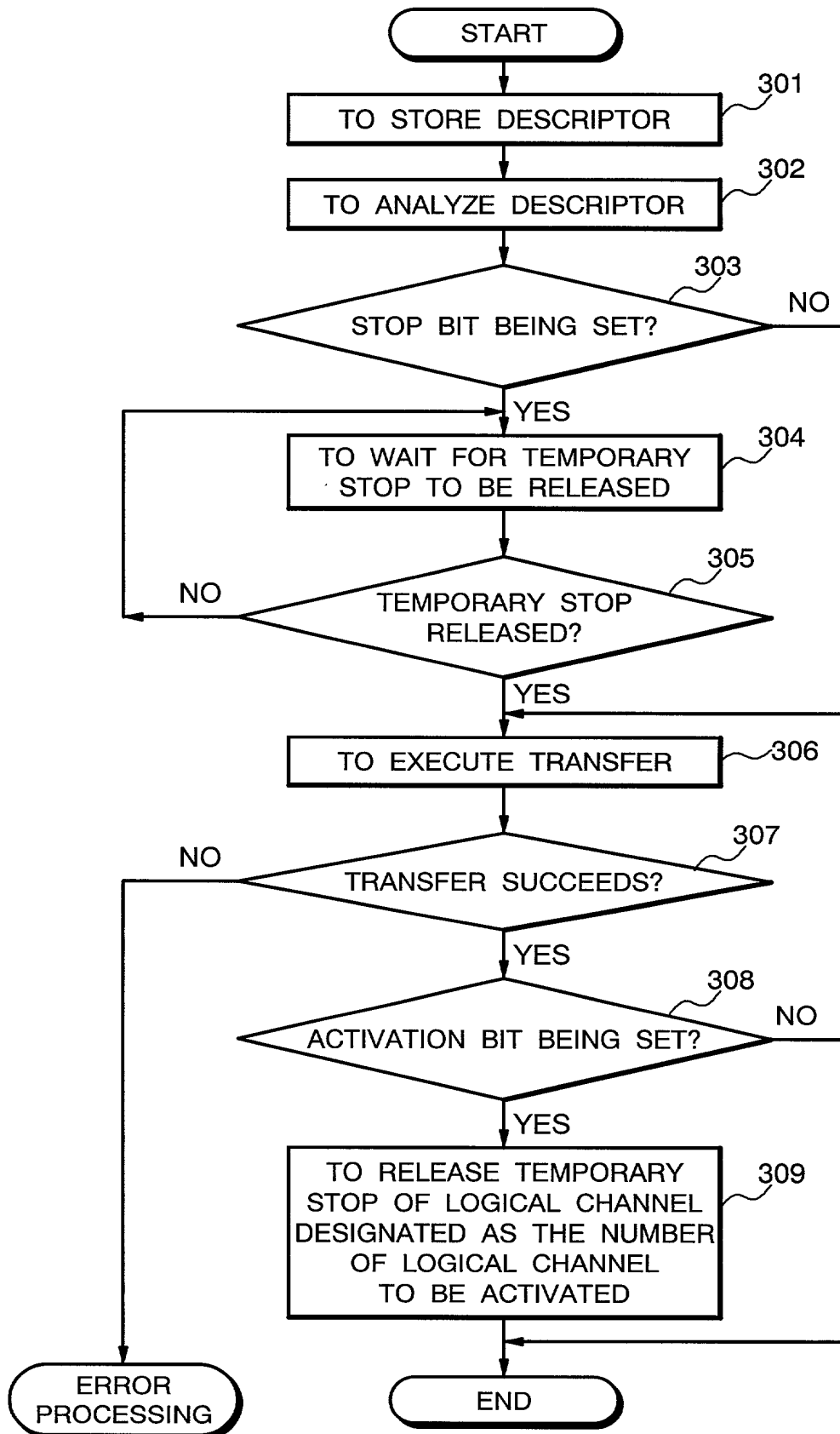
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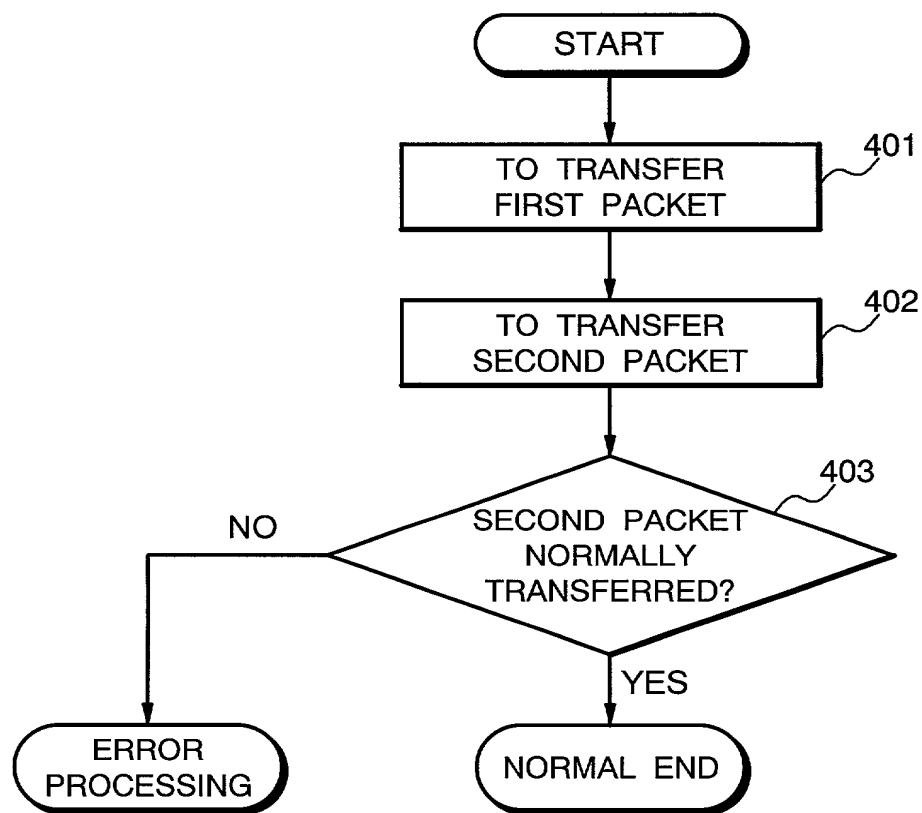
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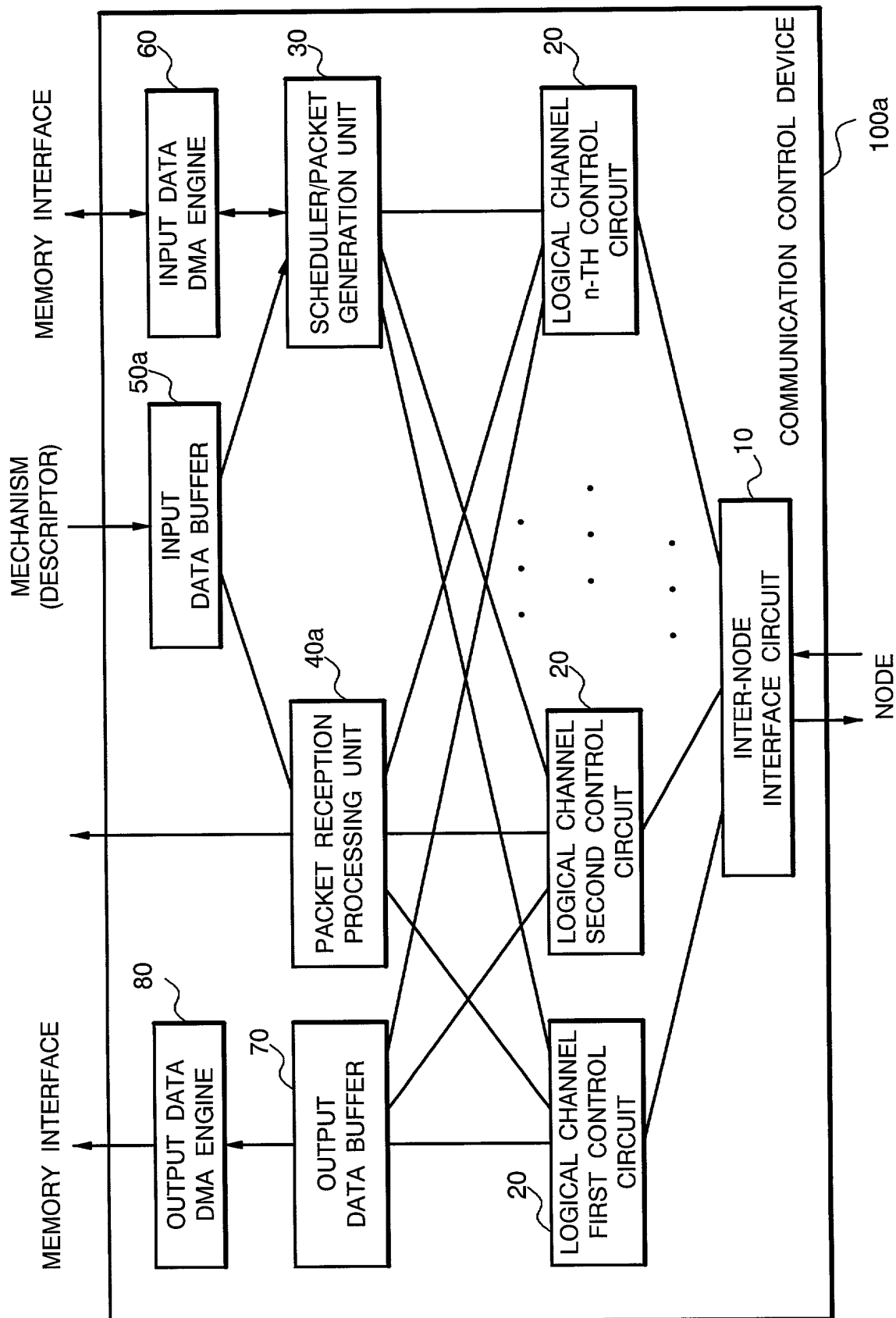


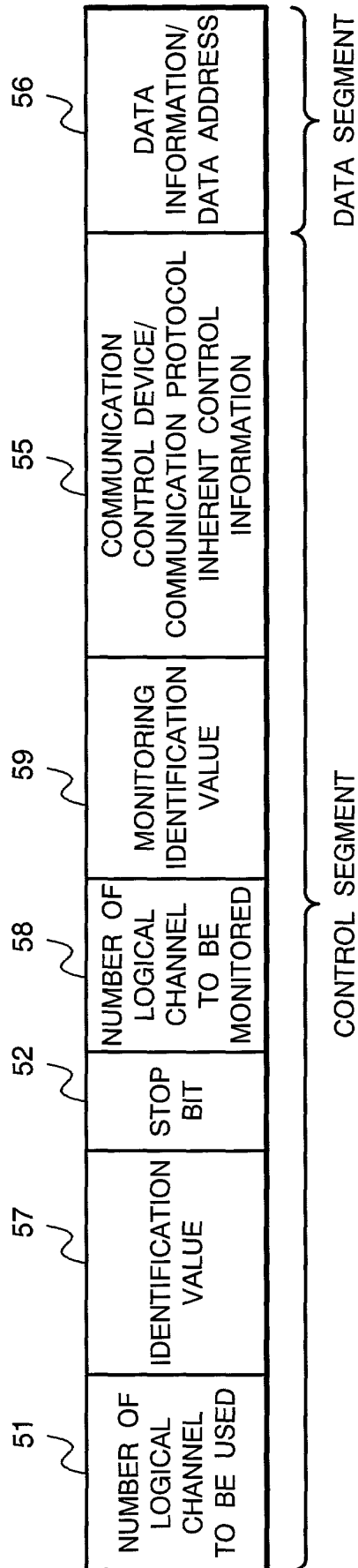
**FIG. 1**

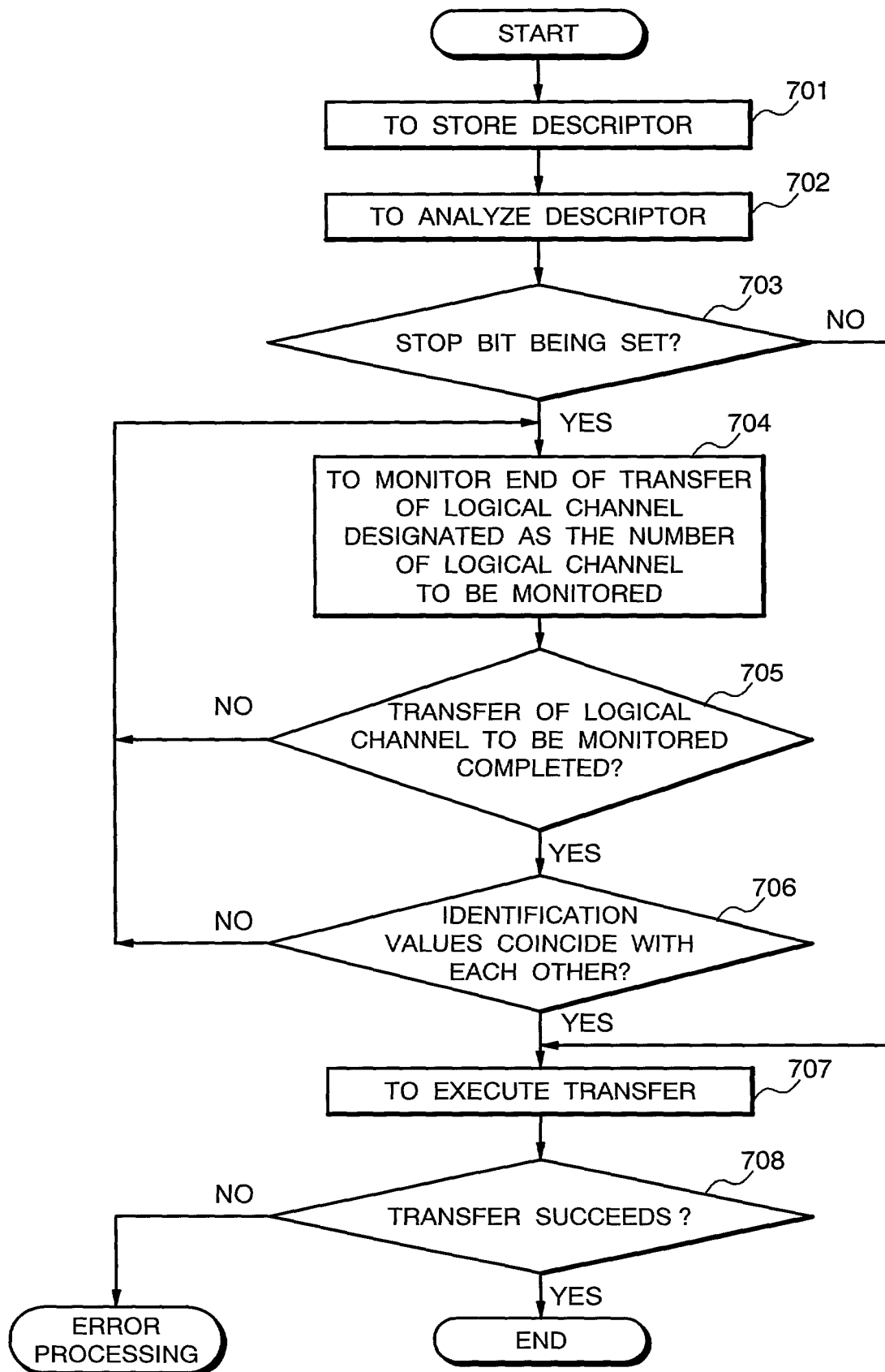
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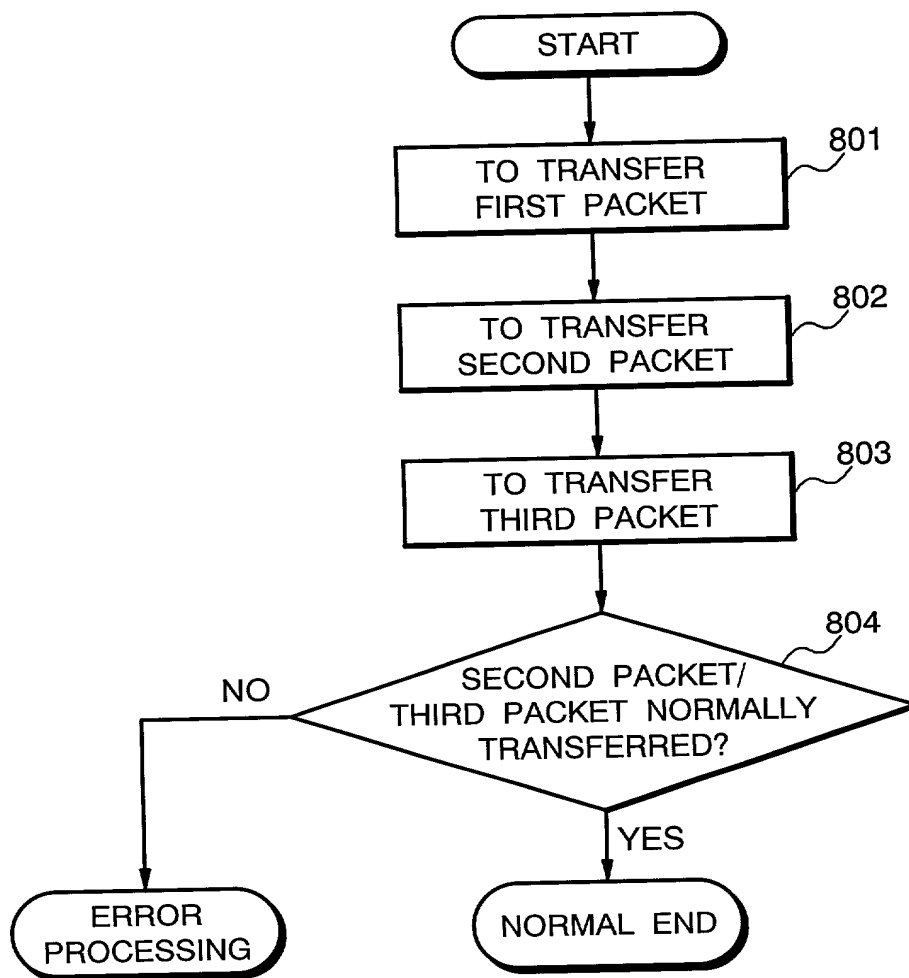
**FIG. 3**

**FIG. 4**

**FIG. 5**

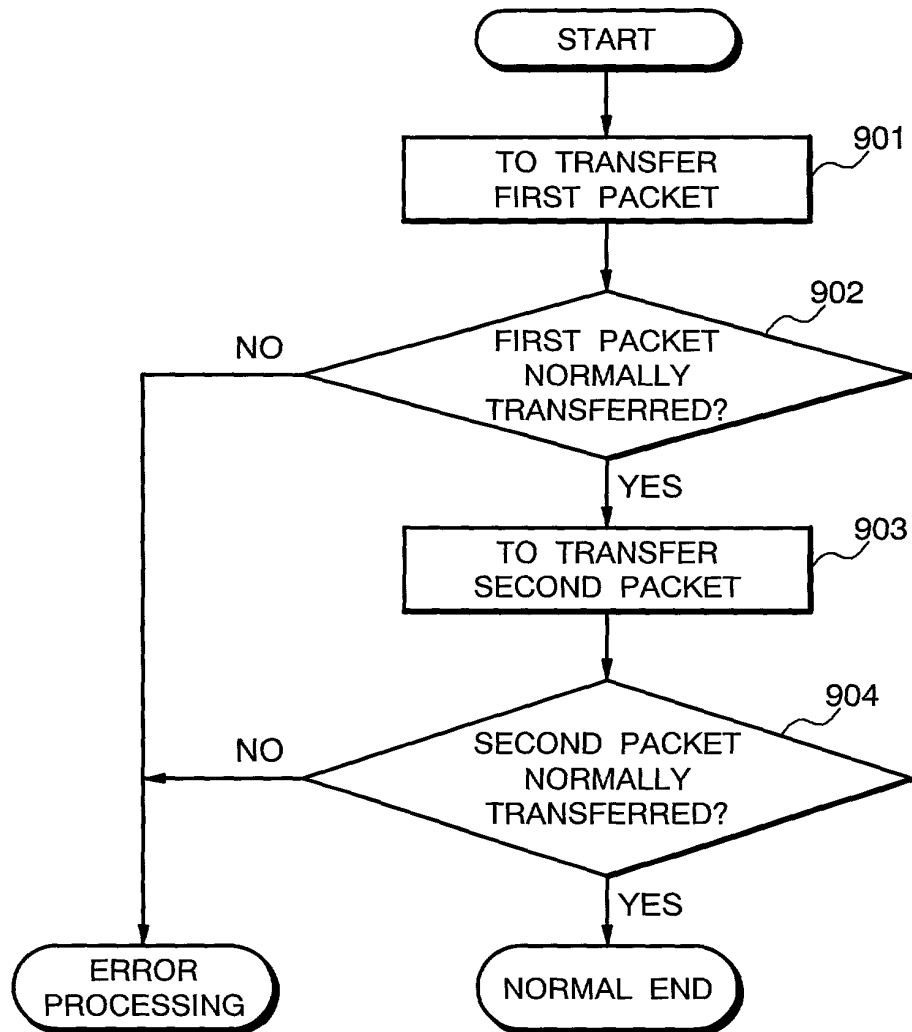
**FIG. 6**

**FIG. 7**

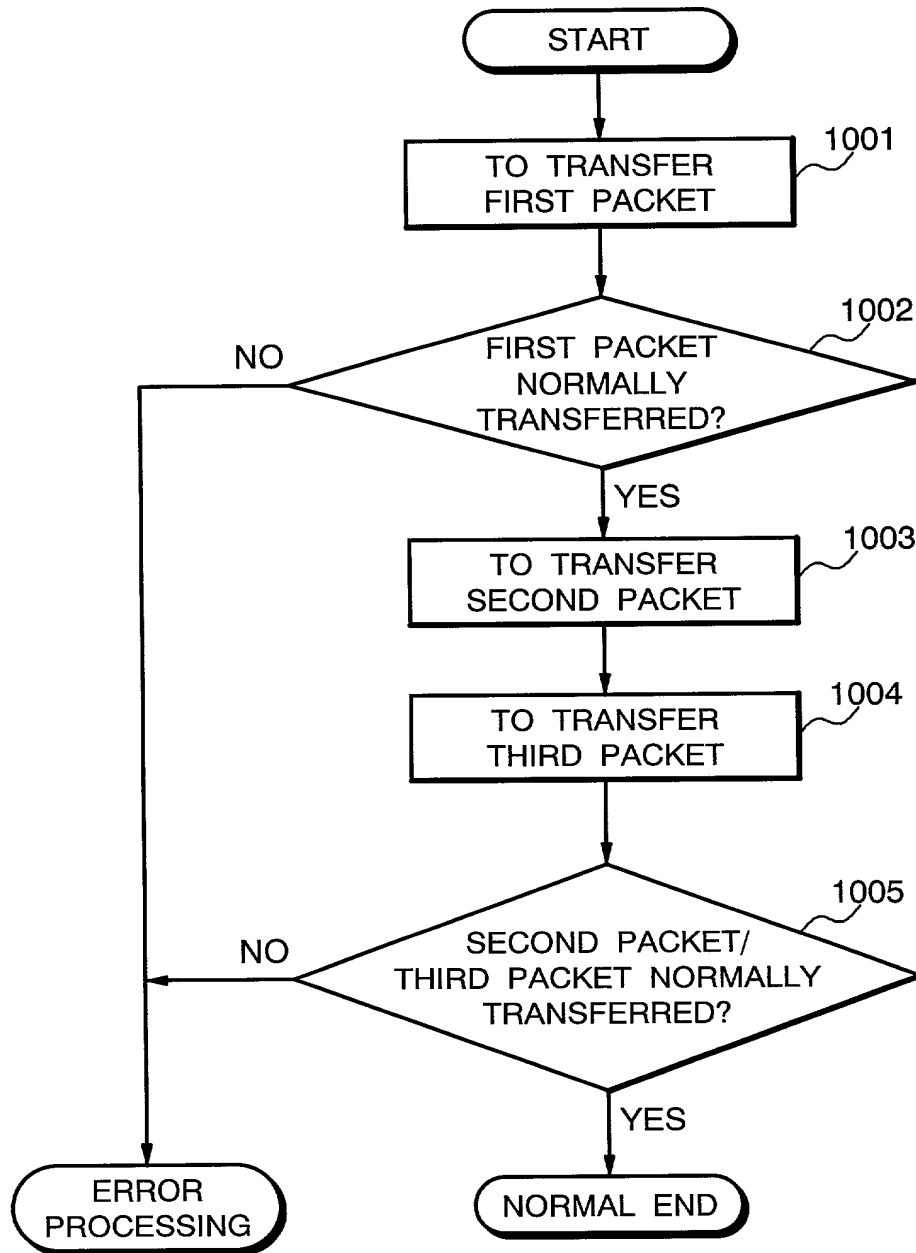
**FIG. 8**



**FIG. 9** (PRIOR ART)



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**FIG. 10**(PRIOR ART)

# Declaration and Power of Attorney for Patent Application

特許出願宣言書

## Japanese Language Declaration

私は、下欄に氏名を記載した発明として、以下の通り宣言する：

私の住所、郵便の宛先および国籍は、下欄に氏名に続いて記載したとおりであり、

名称の発明に関し、請求の範囲に記載した特許を求める主題の本来の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本来の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

COMMUNICATION CONTROL SYSTEM AND

CONTROL METHOD THEREOF

その明細書を  
(該当するほうに印を付す)

☐ ここに添付する。

☐ \_\_\_\_\_ 日に出願番号

第 \_\_\_\_\_ 号として提出し、

\_\_\_\_\_ 日に補正した。  
(該当する場合)

the specification of which  
(check one)

☒ is attached hereto.

☐ was filed on \_\_\_\_\_ as

Application Serial No. \_\_\_\_\_

and was amended on \_\_\_\_\_  
(if applicable)

私は、前記のとおり補正した請求の範囲を含む前記明細書の内容を検討し、理解したことを陳述する。

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

私は、連邦規則法典第37部第1章第56条(a)項に従い、本願の審査に所要の情報を開示すべき義務を有することを認める。

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

# Japanese Language Declaration

私は、合衆国法典第35部第119条、第172条、又は第365条に基づく下記の外国特許出願又は発明者証出願の外国優先権利益を主張し、さらに優先権の主張に係わる基礎出願の出願日前の出願日を有する外国特許出願又は発明者証出願を以下に明記する：

## Prior foreign applications

先の外国出願

333741/1999	Japan	25/November/1999
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)
(Number)	(Country)	(Day/Month/Year Filed)
(番 号)	(国 名)	(出願の年月日)

## Priority claimed

優先権の主張

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Yes	No
あり	なし
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Yes	No
あり	なし
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Yes	No
あり	なし
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Yes	No
あり	なし
<input type="checkbox"/>	<input type="checkbox"/>
Yes	No
あり	なし

私は、合衆国法典第35部第120条に基づく下記の合衆国特許出願の利益を主張し、本願の請求の範囲各項に記載の主題が合衆国法典第35部第112条第1項に規定の様態で先の合衆国出願に開示されていない限りにおいて、先の出願の出願日と本願の国内出願日又はPCT国際出願日の間に公表された連邦規則法典第37部第1章第56条(a)項に記載の所要の情報を開示すべき義務を有することを認める。

I hereby claim the benefit of Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose any material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)
(出願番号)	(出願日)
(Application Serial No.)	(Filing Date)
(出願番号)	(出願日)

(現 況)	(Status)
特許済み、係属中、放棄済み	(patented, pending abandoned)
(現 況)	(Status)
特許済み、係属中、放棄済み	(patented, pending abandoned)

私は、ここに自己の知識に基づいて行った陳述がすべて真実であり、自己の有する情報及び信ずるところに従って行った陳述が真実であると信じ、更に故意に虚偽の陳述等を行った場合、合衆国法典第18部第1001条により、罰金もしくは禁固に処せられるか、又はこれらの刑が併科され、又はかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損なうことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true; and further that all statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

# Japanese Language Declaration

委任状： 私は、下記発明者として、以下の代理人をここに選任し、本願の手続きを遂行すること並びにこれに関する一切の行為を特許商標局に対して行うことを委任する。  
(代理人氏名及び登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

I hereby appoint John H. Mion, Reg. No. 18,879; Donald E. Zinn, Reg. No. 19,046; Thomas J. Macpeak, Reg. No. 19,292; Robert J. Seas, Jr., Reg. No. 21,092; Darryl Mexic, Reg. No. 23,063; Robert V. Sloan, Reg. No. 22,775; Peter D. Olexy, Reg. No. 24,513; J. Frank Osha, Reg. No. 24,625; Waddell A. Biggart, Reg. No. 24,861; Robert G. McMorrow, Reg. No. 19,093; Louis Gubinsky, Reg. No. 24,835; Neil B. Siegel, Reg. No. 25,200; David J. Cushing, Reg. No. 28,703; John R. Inge, Reg. No. 26,916; Joseph J. Ruch, Jr., Reg. No. 26,577; Sheldon I. Landsman, Reg. No. 25,430; Richard C. Turner, Reg. No. 29,710; Howard L. Bernstein, Reg. No. 25,665; Alan J. Kasper, Reg. No. 25,426; Kenneth J. Burchfiel, Reg. No. 31,333; Gordon Kit, Reg. No. 30,764; Susan J. Mack, Reg. No. 30,951; Frank L. Bernstein, Reg. No. 31,484; Mark Boland, Reg. No. 32,197; William H. Mandir, Reg. No. 32,156; Scott M. Daniels, Reg. No. 32,562; Brian W. Hannon, Reg. No. 32,778; Abraham J. Rosner, Reg. No. 33,276; Bruce E. Kramer, Reg. No. 33,725; Paul F. Neils, Reg. No. 33,102; and Brett S. Sylvester, Reg. No. 32,765, my attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and request that all correspondence about the application be addressed to SUGHRUE, MION, ZINN, MACPEAK & SEAS, PLLC, 2100 Pennsylvania Avenue, N.W., Washington, D.C. 20037-3202.

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(Supply similar information and signature for third and subsequent joint inventors.)